

SPECIFICATIONS						
CUSTOMER	:	CUS999				
SAMPLE CODE	:	SH480272T015-IBB				
MASS PRODUCTION CODE	:	PH480272T015-IBB				
SAMPLE VERSION	:	01				
SPECIFICATIONS EDITION	:	003				
DRAWING NO. (Ver.)	:	JLMD-PH480	272T015-IBB_001			
PACKAGING NO. (Ver.)	:	JPKG- PH480	0272T015-IBB_002			
	Customer	Approved	Date:			
Approved	Che		Designer			
李昀	Checked 劉進		陳璐			
Preliminary specificationSpecification for sample a	approval					
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History of Version

Date	Ver.	Edi.	Description	Page	Design by
08/22/2019	01	001	New Drawing.	-	任健
11/13/2019	01	002	New Sample.	-	俞承澤
12/02/2019	01	003	Modify LCM Packaging	Appendix	陳璐
				1	Total: 35 Pa



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Note: For detailed information please refer to IC data sheet: Sitronix --- ST7282-G4



1. SPECIFICATIONS

1.1 Features

Item	Standard Value			
Display Type	480 * 3(RGB) * 272 Dots			
LCD Type	a-Si TFT, Positive/Normally white TN, Transmissive Type			
Screen size(inch)	4.3"(Diagonal)			
Viewing Direction	6 O'clock (Gray scale Inversion)*1			
Viewing Direction	12 O'clock (*2)			
Color configuration	R,G,B vertical stripe			
Display Interface	Digital 24-bits RGB			
Driver IC	ST7282-G4			
	THIS PRODUCT CONFORMS THE ROHS OF PTC			
ROHS	Detail information please refer website :			
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/			

*1. For saturated color display content (e.g. pure-red, pure-green, pure-blue or pure-colors -combinations).

*2. "For display content based upon multicolor images e.g. photos, RGB defined user interfaces".

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	105.5(W) x 67.2(L) x 4.0(H)	mm

LCD panel

Item	Standard Value		
Active Area	95.04 (W) x 53.856 (L)	mm	
Pixel Size	0.198 (W) * 0.198 (H)	mm	

Note: For detailed information please refer to LCM drawing



1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
System Power Supply Voltage	V _{DD}	GND=0	-0.3	+4.6	V
Input Voltage Range	V _{IN}	-	-0.3	V _{DD} +0.3	V
Operating Temperature	T _{OP} (Ts)	Note 1	-20	+70	°C
Storage Temperature	Ts⊤(Ta)	Note 2	-30	+80	°C
Storage Humidity	HD	Ta ≦ 60 °C	10	90	%RH

The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 1 : Ts is the temperature of panel's surface.

Note 2 : Ta is the ambient temperature of samples

1.4 DC Electrical Characteristics

$GND = 0V Ta = 25^{\circ}C$

Module				GND =	0V, Ta = 25°	°C
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Power supply Voltage	V _{DD}		3.0	3.3	3.6	V
"H" Input Voltage	Vih	-	0.7*V _{DD}	-	V _{DD}	V
"L" Input Voltage	VIL		GND	-	0.3*V _{DD}	V
"H" Output Voltage	Vон	-	V _{DD} -0.4	-	V _{DD}	V
"L" Output Voltage	Vol	-	GND	-	GND+0.4	V
Supply Current	IDD	V _{DD} =3.3V	-	30	50	mA



1.5 Optical Characteristics

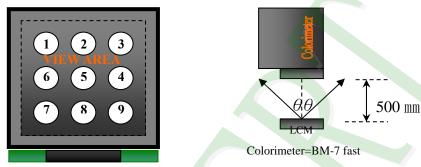
VDD =3.3V, Ta=25°C

Item		Symbol	Condition	Min.	Тур.	Max.	unit	
Response tin	ne	Tr + Tf	-	-	29	44	ms	Note2
	Тор	θY+		-	60	-		
	Bottom	θY-	CD > 10	-	60	-		Niete (
Viewing angle	Left	θХ-	CR ≥ 10	-	60	-	Deg.	Note4
	Right	θX+		-	60	-		
Contrast rati	0	CR	-	500	600	-	-	-
	White	Х		0.26	0.31	0.36		
	vvnite	Y		0.26	0.31	0.36		
	Red	Х		0.56	0.61	0.66		Note1
Color of CIE Coordinate	Reu	Y	IF= 40 mA	0.31	0.36	0.41		
(With B/L&T/P)	Green	Х	IF- 40 IIIA	0.30	0.35	0.40	-	NOLET
	Gleen	Y		0.51	0.56	0.61		
	Blue	X		0.10	0.15	0.20		
	Diue	Y		0.00	0.05	0.10		
Average Brightr	ness							
Pattern=white di	splay	IV	IF= 40 mA	450	610	-	cd/m²	Note1
(With LCD&T/P)*1							
Uniformity (With LCD&T/P	2) *2	ΔB	IF= 40 mA	70	-	-	%	Note1



Note1:

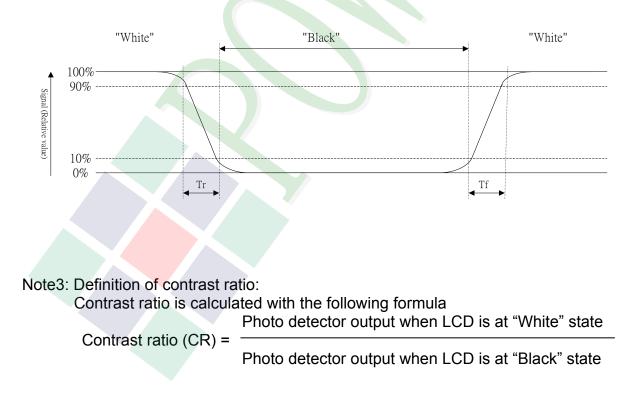
- $1: \triangle B=B(min) / B(max) \times 100\%$
- 2 : Measurement Condition for Optical Characteristics:
 - a : Environment: 25°C ±5°C / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: 500 ± 50 $_{\rm mm}$, (0= 0°)
 - c : Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.
 - d: The uncertainty of the C.I.E coordinate measurement ±0.01 , Average Brightness ± 4%



Note2: Definition of response time:

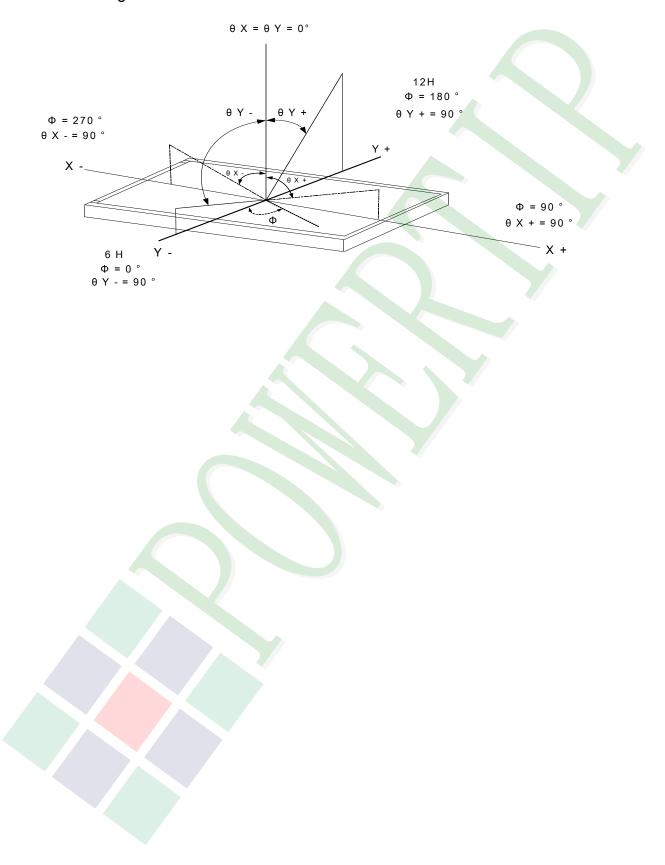
The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:





Note4: Definition of viewing angle: Refer to figure as below:





1.6 Backlight Characteristics

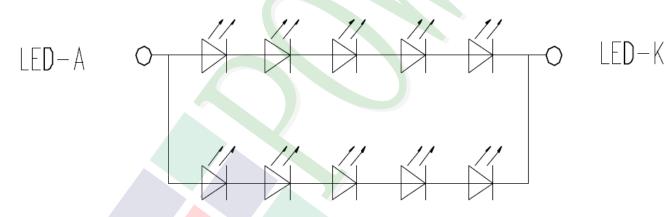
Maximum Ratings

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
LED Forward Current	IF	Ta =25℃	-	-	60	mA
LED Reverse Voltage (Each one)	VR	Ta =25℃	-	<u> </u>	5	V
Power Dissipation	PD	Ta =25℃	-	-	1020	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF		13.5	15.0	17.0	V
Average Brightness (Without LCD & T/P)	IV	IF=40mA	9500	11000	14000	cd/m ²
CIE Color Coordinate	Х		0.26	0.28	0.32	
(Without LCD & T/P)	Y		0.26	0.28	0.32	-
Color			White			

Internal Circuit Diagram



Other Description

Item	Conditions	Description
Life Time*1	Ta =25℃	20,000 hrs
	IF= 40mA	20,000 ms



1.7 Touch Panel Characteristics

1.7.1 Optical Characteristics

Item	Specification
1.Transparency	80% Min

1.7.2 Mechanical Characteristic

Item	Specification
1.Input Method	Finger or stylus pen
2.Hardness of surface	3H -pressure 500g of ,45deg.
3.FPC peeling strength	500gf min(Peeling upward by 90°)
4.Activation Force	50gf~120gf individual point with stylus pen(R0.8)
	Activation force guarantee area: 3.0mm inside of Active Area.
5.Linearity Force	80gf less input with stylus pen(R0.8)
	Activation force guarantee area: 3.0mm inside of Active Area.

1.7.3 Electrical Characteristics

Item	Specification
1.Rated Voltage	DC 5V(DC 7V Max)
2. Resistance Between	Direction X (Glass side): 500Ω~ 1250Ω
Terminals.	Direction Y (Film side): 150Ω~ 450Ω
3.Insulation Resistance	20 MΩ or more (DC 25 V)
4.Linearity	≤±1.5%.
	Linearity(%)= ΔV/(EV-SV)*100.
	ΔV : The difference between the ideal voltage and measured voltage on the each measuring line.
	SV: Voltage of starting Points.
	EV: Voltage of Ending Points.
	(Test condition refers to 1.7.2 item5)
5.Bouncing	<10ms



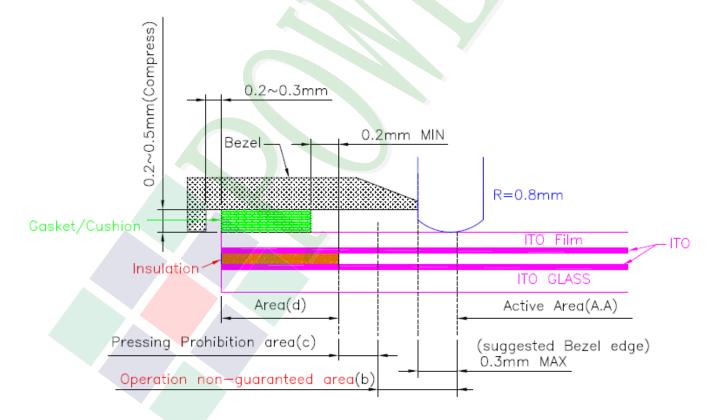
1.7.4 Reliability Characteristic

NO	Test Item	Test Condition	Test Result	
		1,000,000times min. (R 8 mm	Follow 1.7.3 item2 and item4.	
1	Hitting Durability	Silicon Rubber Hardness 60°250gf		
		2times/sec).		
2	Pen Sliding Durability	100,000 times min (Tip R0.8mm).	Follow 1.7.3 item2 and item4.	
		ψ9mm steel ball is dropped on the	No crack	
3	Impact Resistance	surface from 30 cm height at 1		
		time.		
4	Flexible pattern Bending	Bending 3 times by bending	Follow 1.7.3 item2.	
4	Resistance	radius R1.0 mm		
5	Flexible Pattern Insert/Pull	5times at least.	Follow 1.7.3 item2.	
Э	Out Resistance	Stimes at least.		



1.7.5 Touch Panel Design/Handing Guide

- (1) Keep the gap, for example 0.2 to 0.3mm, between bezel edge and T/P edge. The reason is to avoid the bezel edge from contacting T/P surface that may cause "short" with bottom layer
- (2) Insertion a cushion material is recommended.
- (3) The cushion material should be limited on the busbar insulation paste area. If it is over the transparent insulation paste area, a "short" may be occurred.
- (4) Do not to use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- (5) Never expand the T/P top layer (PET Film) like a balloon by internal air pressure. The life of the T/P will extremely decreasing.
- (6) Top layer, PET, dimension is changing base on environment temperature and humidity. Please avoid a stress from housing bezel to top layer, because it may cause "waving".
- (7) The input to the Touch Panel sometimes distorts touch panel itself.
- (8)To use the stylus pen or fingernail sliding at the edge of the housing is prohibited. It would cause the cracking of the ITO coating and damage the touch panel. It also request not to press this area while assembling
- (9) Purpose: In order to prevent accidental use and performance deterioration, please keep the following precautions.



In order to prevent unusual performance degradation and malfunction of a touch panel, please carry out the set case designing and a touch panel assembling method after surely considering the definition of each area illustrated in above figure



Area(a) : Active area

The active area is guaranteed the position data detectable precision, operation force and other operations. it is strongly recommended to place the operation button or menu keys within the active area. Due to structure, the active area is less durable at the edge or close to the edge.

Area(b) : Operation non-guaranteed area

This area does not guarantee a touch panel operation and its function. When this area is pressed, touch panel shows degradation of its performance and durability such as a pen sliding durability becomes about one-tenth compared with the active area (area-(a) as guaranteed area) and its operation force requires about double. About 0.5 mm outside from a boundary of the active area corresponds to this area.

Area(c) : Pressing prohibition area

The area which forbids pressing, because an excessive load is applied to a transparent electrode (ITO) and a serious damage is given to a touch panel function by pressing. About 0.5 mm outside from Operation non-guaranteed area.

Area(d) : Non-Active area The area does not activate even if pressed.



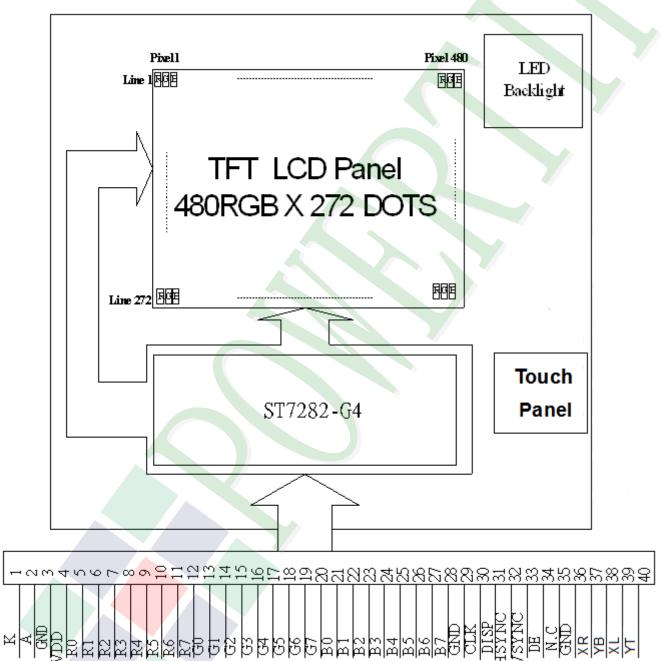
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

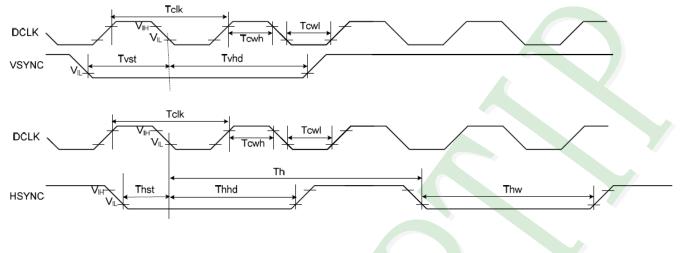
Pin No.	Symbol	Function
1	К	Power supply for LED Backlight cathode input
2	А	Power supply for LED Backlight anode input
3	GND	Ground
4	VDD	Digital power
5	R0	Red data bit 0
6	R1	Red data bit 1
7	R2	Red data bit 2
8	R3	Red data bit 3
9	R4	Red data bit 4
10	R5	Red data bit 5
11	R6	Red data bit 6
12	R7	Red data bit 7
13	G0	Green data bit 0
14	G1	Green data bit 1
15	G2	Green data bit 2
16	G3	Green data bit 3
17	G4	Green data bit 4
18	G5	Green data bit 5
19	G6	Green data bit 6
20	G7	Green data bit 7

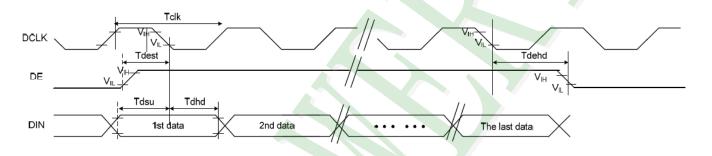
PO	WERTL	р
Pin No.	Symbol	Function
21	В0	Blue data bit 0
22	B1	Blue data bit 1
23	B2	Blue data bit 2
24	B3	Blue data bit 3
25	B4	Blue data bit 4
26	B5	Blue data bit 5
27	B6	Blue data bit 6
28	B7	Blue data bit 7
29	GND	Ground
30	CLK	Dot data clock
31	DISP	Display control / standby mode selection "High": Normal display
32	HSYNC	Horizontal sync input
33	VSYNC	Vertical sync input
34	DE	Data input enable. Active High to enable the data input
35	N.C	Not Connect
36	GND	Ground
37	XR	Right side of touch panel
38	YB	Bottom side of touch panel
39	XL	Left side of touch panel
40	YT	Up side of touch panel



2.3 Timing Characteristics

2.3.1 Clock and Data Input Timing Diagram



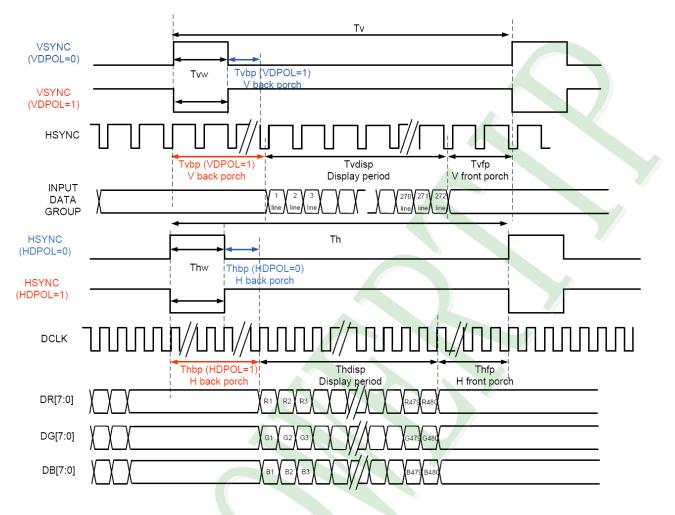




Deremetere	Cumbal		Spec		Unit	Conditions			
Parameters	Symbol	Min.	Тур.	Max.	Unit	Conditions			
System operation timing									
VDD power source slew time	TPOR	-	-	20	ms	From 0V to 99% VDD			
GRB pulse width	t RSTw	10	50	-	us	R=10Kohm, C=1uF			
	I	nput/ C	Dutput	timing	4				
CLK pulse duty	Tcw	40	50	60	%	-			
Hsync width	Thw	2	-	-	DCLK	-			
HSYNC period	Th	55	60	65	us	-			
VSYNC setup time	Tvst	12	-	-	ns	-			
VSYNC hold time	Tvhd	12	-	-	ns	_			
HSYNC setup time	Thst	12	-	-	ns	-			
HSYNC hold time	Thhd	12	-		ns	-			
Data setup time	Tdsu	12	-		ns	<u> </u>			
Data hold time	Tdhd	12	-	-	ns	-			
DE setup time	Tdest	10	-	-	ns	-			
DE hold time	Tdehd	10	-	-	ns	-			
SD output stable time	Tst	-	-	12	us	Output settled within +20mV Loading =.6.8k+28.2pF			
GD output rise and fall time	Tgst	-	_	6	us	Output settled (5%~95%), Loading = 4.7k+29.8pF			

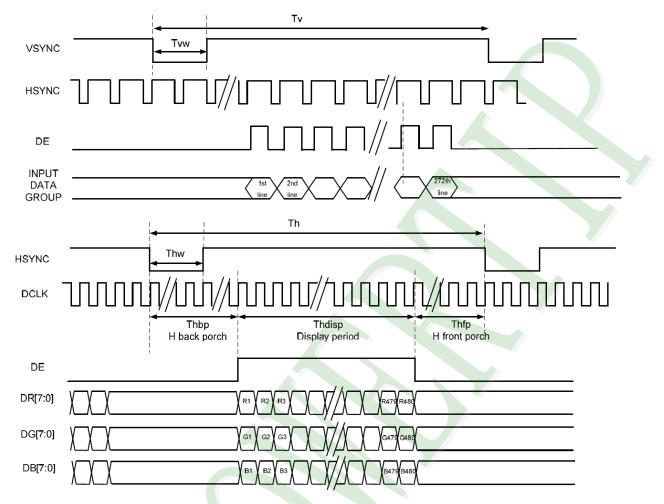


2.3.2 SYNC Mode



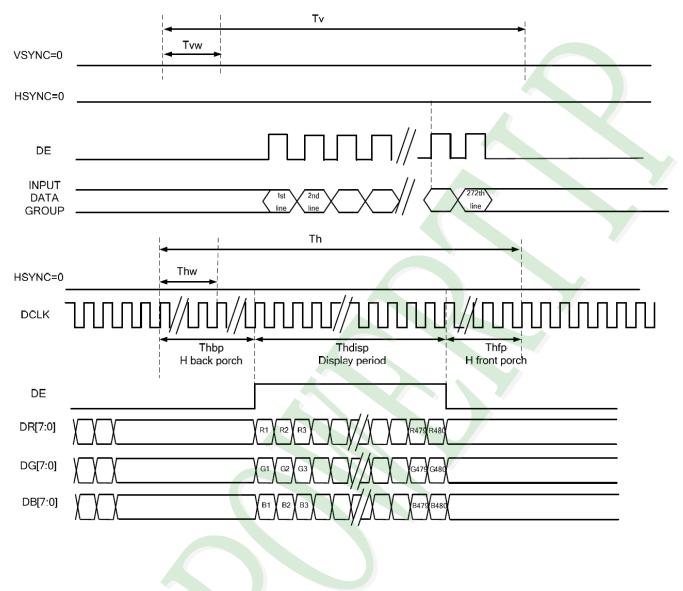


2.3.3 SYNC-DE Mode





2.3.4 DE Mode





Parallel 24-bit RGB Input Timing Table

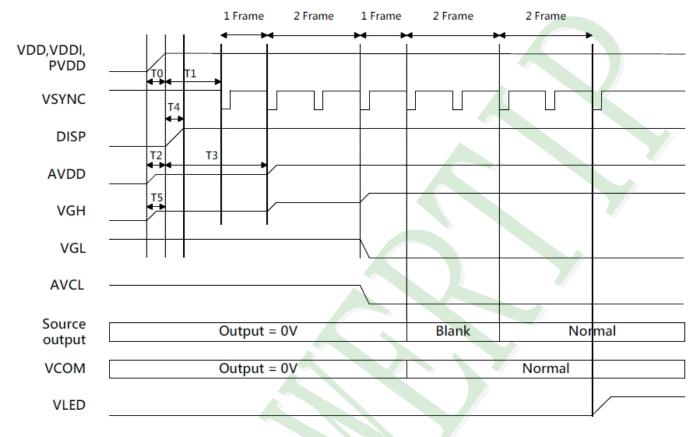
Parar	Symbol		Unit			
Fala	Symbol	Min.	Тур.	Max.	Onit	
DCLK frequency	DCLK frequency			9	12	MHz
DCLK Period		Tclk	83	111	125	nS
	Period Time	Th	485	531	598	DCLK
	Display Period	Thdisp	-	480	-	DCLK
HSYNC	Back Porch	Thbp	3	43	43	DCLK
	Front Porch	Thfp	2	8	75	DCLK
	Pulse Width	Thw	2	4	75	DCLK
	Period Time	Tvdisp	276	292	321	Н
	Display Period	Tvbp		272		Н
VSYNC	Back Porch	Tvfp	2	12	12	Н
	Front Porch	Tvw	2	8	37	Н
	Pulse Width	Tvdisp	2	4	37	Н





2.4 POWER ON/OFF SEQUENCE

2.4.1 Power On Sequence

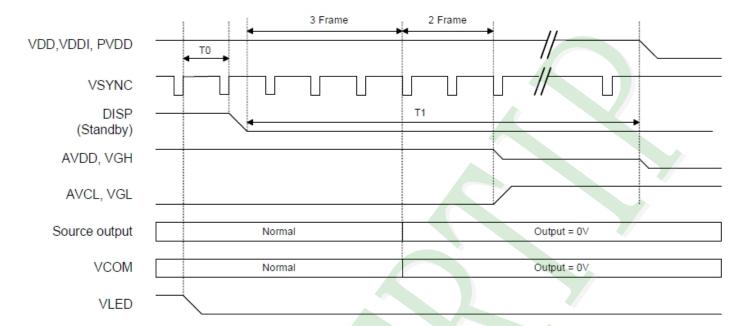


Symbol	Description	Min. Time
Т0	Determined by the external power	
T1	Time from stable VDD, VDDI, PVDD set-up to the first VSYNC	T1=0
T2	Time from AVDD=0V to AVDD=3.3V	T2=T0
Т3	me from AVDD=3.3V to AVDD=6.0V	T3=T1+ (1*Frame)
T4	Time from stable VDD, VDDI, PVDD set-up to DISP asserted	T4=0
Т5	Time from VGH=0V to VGH=3.3V	T5=T0

Note: Recommend the LCM power on rise time T0= 0~1ms.



2.4.2 Power Off Sequence



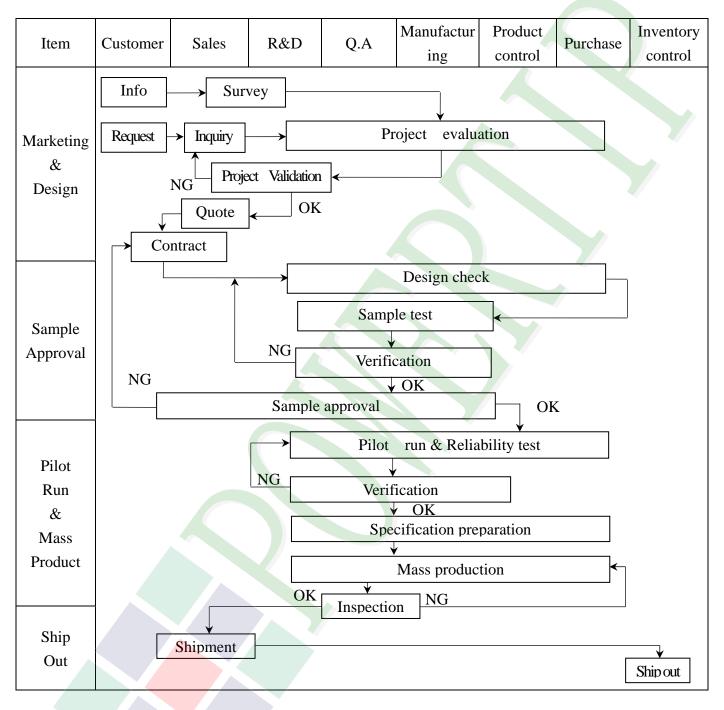
Symbol	Description	Min. Time
Т0	Time from backlight power off to DISP="L"	1*Frame
T1	Time from DISP="L" to LCM Power off	5*Frame





3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



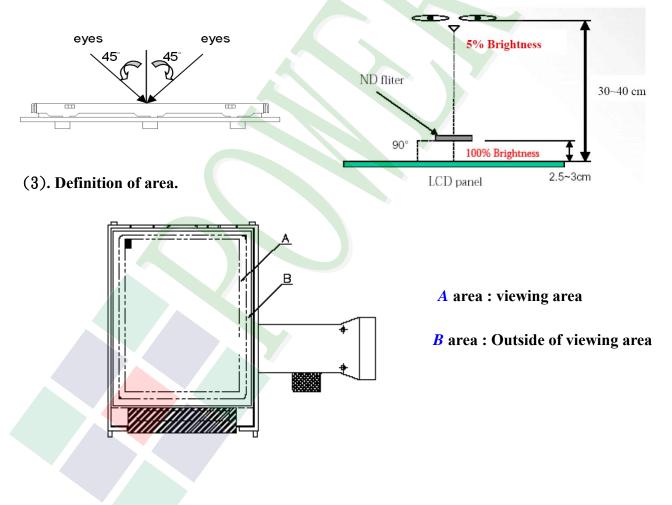


Item	Customer	Sales	R&D	Q.A	Manufactu ring	Product control	Purchase	Inventory control
	Info	→ Claim –	ſ		Failure an	alvsis	5	
Sales	Analys	sis report	L		•			
Service			L		Corrective	action		
				Trackin	g_←			
	1 100 0001		A	<u> </u>				
Q.A Activity	 ISO 9001 Equipment Standardi 	nt calibration	n		ocess impro Education Ar			

POWERTIP

3.2 Inspection Specification

- ◆Scope: The document shall be applied to TFT-LCD Module for 3. 5″~15″ (Ver.B01).
- ◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- Equipment : Gauge \ MIL-STD \ Powertip Tester \ Sample
- ◆Defect Level : Major Defect AQL : 0.4 ; Minor Defect AQL : 1.5
- **OUT Going Defect Level** : Sampling.
- ◆Standard of the product appearance test :
 - a. Manner of appearance test :
 - (1). The test best be under 20W×2 fluorescent light(about 300lux ~500lux)
 - and distance of view must be at 30~40 cm.
 - (2). The test direction is base on about around 45° of vertical line.



(4). Standard of inspection : (Unit : mm)

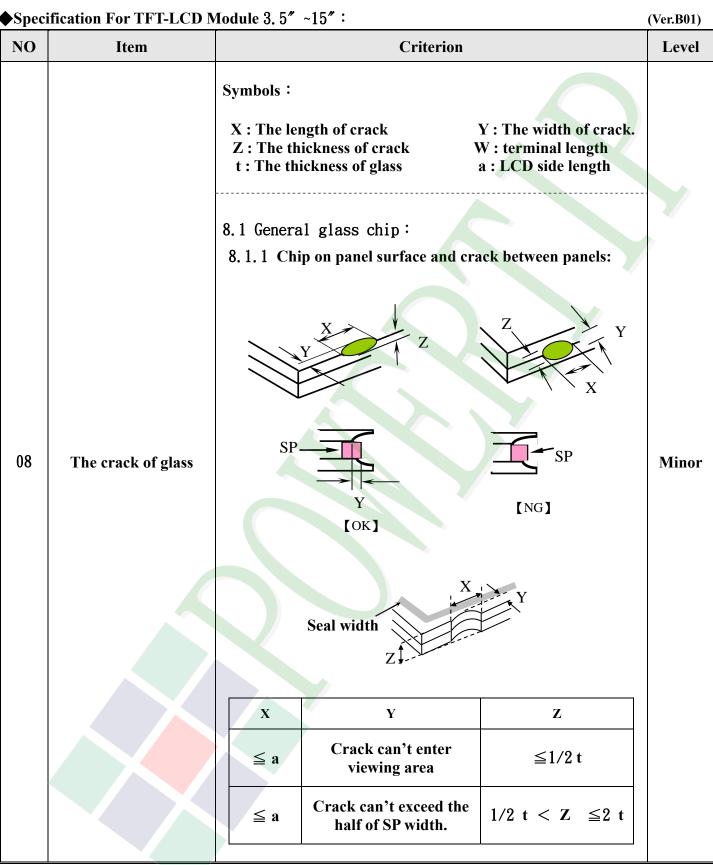


◆ Spe	cification For TFT-L	CD Module 3. 5″~15″:	Ver.B01)				
NO	Item	Criterion					
		1. 1The part number is inconsistent with work order of production.					
01	Product condition	1. 2 Mixed product types.	Major				
		1. 3 Assembled in inverse direction.	Major				
02	Quantity	2. 1The quantity is inconsistent with work order of production.	Major				
03	Outline dimension	3.1 Product dimension and structure must conform to structure diagram.	Major				
		4. 1 Missing line character and icon.	Major				
		4. 2 No function or no display.	Major				
	Electrical Testing	4. 3 Display malfunction.	Major				
04		4. 4 LCD viewing angle defect.					
		4. 5 Current consumption exceeds product specifications.					
		4. 6 Mura can not be seen through 5% ND filter at 50% Gray screen , should be judged by the viewing angle of 90 degree.	Minor				
		Item Acceptance (Q'ty)					
		Bright Dot ≤ 4					
	Dot defect	DotDark Dot ≤ 5					
		DefectJoint Dot ≤ 3					
05	(Bright dot `	Total ≤ 7	2.51				
05	Dark dot)		Minor				
	On -display	5.1 Inspection pattern : full white , full black , Red , Green and					
	On -uispiay	blue screens.					
		5. 2 It is defined as dot defect if defect area $>1/2$ dot.					
		5. 3 The distance between two dot defect ≥ 5 mm.					
		5.4 Bright dot that can not be seen through 5% ND filter.					



◆ Spe	cification For TFT	F-LCD Module 3. 5″~15″:	(Ver.B01)	
NO	Item	Criterion		
06	Black or white dot \cdot scratch \cdot contamination Round type $\downarrow X \qquad \downarrow Y \qquad \downarrow$ $\Phi = (x+y)/2$ Line type $\downarrow L \qquad \downarrow W \qquad \downarrow$	6. 1 Round type (Non-display or display): $ Dimension (diameter : \Phi) \qquad Acceptance (Q'ty) \\ A area \qquad B area \\ \hline \Phi \le 0.25 \qquad Ignore \\ \hline 0.25 < \Phi \le 0.50 \qquad 5 \qquad Ignore \\ \hline 0.25 < \Phi \ge 0.50 \qquad 0 \qquad Ignore \\ \hline 0.25 < \Phi \ge 0.50 \qquad 0 \qquad Ignore \\ \hline 0.25 < \Phi \ge 0.50 \qquad 0 \qquad Ignore \\ \hline 0.25 < \Phi \ge 0.50 \qquad 0 \qquad Ignore \\ \hline 0.25 < \Phi \ge 0.50 \qquad 0 \qquad Ignore \\ \hline 0.25 < \Phi \ge 0.50 \qquad 0 \qquad Ignore \\ \hline 10000000000000000000000000000000000$	Minor	
07	Polarizer Bubble	Dimension (diameter : Φ)Acceptance (Q'ty) $A area$ $B area$ $\Phi \leq 0.25$ Ignore $0.25 < \Phi \leq 0.50$ 4 $0.50 < \Phi \leq 0.80$ 1 $\Phi > 0.80$ 0Total5	Minor	







◆Specification For TFT-LCD Module 3. 5″~15″: (Ver.B01)					
NO	Item	Criterion			
		Symbols :X : The length of crackY : The width of crack.Z : The thickness of crackW : terminal lengtht : The thickness of glassa : LCD side length8. 1. 2 Corner crack : $X = \frac{Z}{Y}$			
		XYZ $\leq 1/5$ aCrack can't enter viewing areaZ $\leq 1/2$ t			
		$\leq 1/5 \text{ a} \begin{array}{c} \text{Crack can't exceed the} \\ \text{half of SP width.} \end{array} 1/2 \text{ t} < \text{Z} \leq 2 \text{ t} \end{array}$			
08	The crack of glass	8.2 Protrusion over terminal:	Minor		
		8.2.1 Chip on electrode pad: X Y Z X Y Z Z X Y Z			
		X			
		XYZFront $\leq a$ $\leq 1/2$ W $\leq t$			
		Back $\leq a$ $\leq W$ $\leq 1/2 t$			



Specification For TFT-LCD Module 3. 5" ~15" : (Ver.B01) NO Criterion Level Item Symbols : **X** : The length of crack Y: The width of crack. **Z** : The thickness of crack W: terminal length t : The thickness of glass a : LCD side length 8.2.2 Non-conductive portion: Х Y Z $\leq W$ $\leq 1/3$ a ≦t The crack of 08 Minor glass \odot If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. 8.2.3 Glass remain : Pitch X Y Ζ $\leq 1/3$ W ≦t ≦ a 8.2.4 Cracking Not Allowed



◆Specification For TFT-LCD Module 3. 5″~15″:

Specification For TFT-LCD Module 3. 5″~15″ :				
NO	Item	Criterion	Level	
	Backlight elements	9. 1 Backlight can't work normally.	Major	
09		9. 2 Backlight doesn't light or color is wrong.	Major	
		9. 3 Illumination source flickers when lit.	Major	
	General appearance	10. 1 Pin type < quantity < dimension must match type in structure diagram.	Major	
		10. 2 No short circuits in components on PCB or FPC .	Major	
		10.3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major	
10		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor	
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor	
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤ 1.5 mm.	Minor	



4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

- T . I				
NO.	TEST ITEM	TEST CONDITION		
1	High Temperature	Keep in +80 ±2°C 240 hrs		
-	Storage Test	Surrounding temperature, then storage at normal condition 4hrs.		
2	Low Temperature	Keep in $-30 \pm 2^{\circ}$ C 240 hrs		
	Storage Test	Surrounding temperature, then storage at normal condition 4hrs.		
3	High Temperature /	Keep in +60 ℃ / 90% R.H duration for 240 hrs		
J	High Humidity Storage Test	Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)		
			\rightarrow +80°C \rightarrow +25°C	
-	Temperature Cycling	(30mins) (5mins)	(30mins) (5mins)	
4	Storage Test	20 Cycle		
		Surrounding temperature, then sto	orage at normal condition 4hrs.	
		Air Discharge:	Contact Discharge:	
		Apply 2 KV with 5 times	Apply 250 V with 5 times	
		Discharge for each polarity +/-	discharge for each polarity +/-	
		1. Temperature ambiance : 15℃ ~35℃		
5	ESD Test	2. Humidity relative : 30%~60%		
		3. Energy Storage Capacitance(Cs+Cd) : 150pF±10%		
		4. Discharge Resistance(Rd) : $330 \Omega \pm 10\%$		
		5. Discharge, mode of operation :		
		Single Discharge (time between successive discharges at least 1 sec)		
		(Tolerance if the output voltage inc	dication : ±5%)	
	Vibration Test	1. Sine wave	$10 \sim 55$ Hz frequency (1 min/sweep)	
6	(Packaged)	2. The amplit	ude of vibration :1. 5 mm	
	(I ackageu)	3. Each direction (X · Y · Z) duration for 2 Hrs		
		Packing Weight (Kg	Drop Height (cm)	
		0 ~ 45.4	122	
-	Drop Test	45.4 ~ 90.8	76	
7	(Packaged)	90.8 ~ 454	61	
		0ver 454	46	
		Drop Direction : ※1 corner / 3 edg	es / 6 sides each 1time	

©Result Evaluation Criteria :

Under the display quality test conditions with normal operations with normal operation state. Do not change these conditions as such changes may affect practical display function. (Normal operation state)

Temperature : +20~30°C Humidity : 50~70%

Atmospheric pressure: 86~106Kpa



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—when working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320\pm10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.
- 5.2.10 Caution! (LCM products with Capacitive Touch Panel) Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).

Therefore, the touch needs to be thoroughly tested inside the target application.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}_{\circ} \pm 5^{\circ}_{\circ}$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

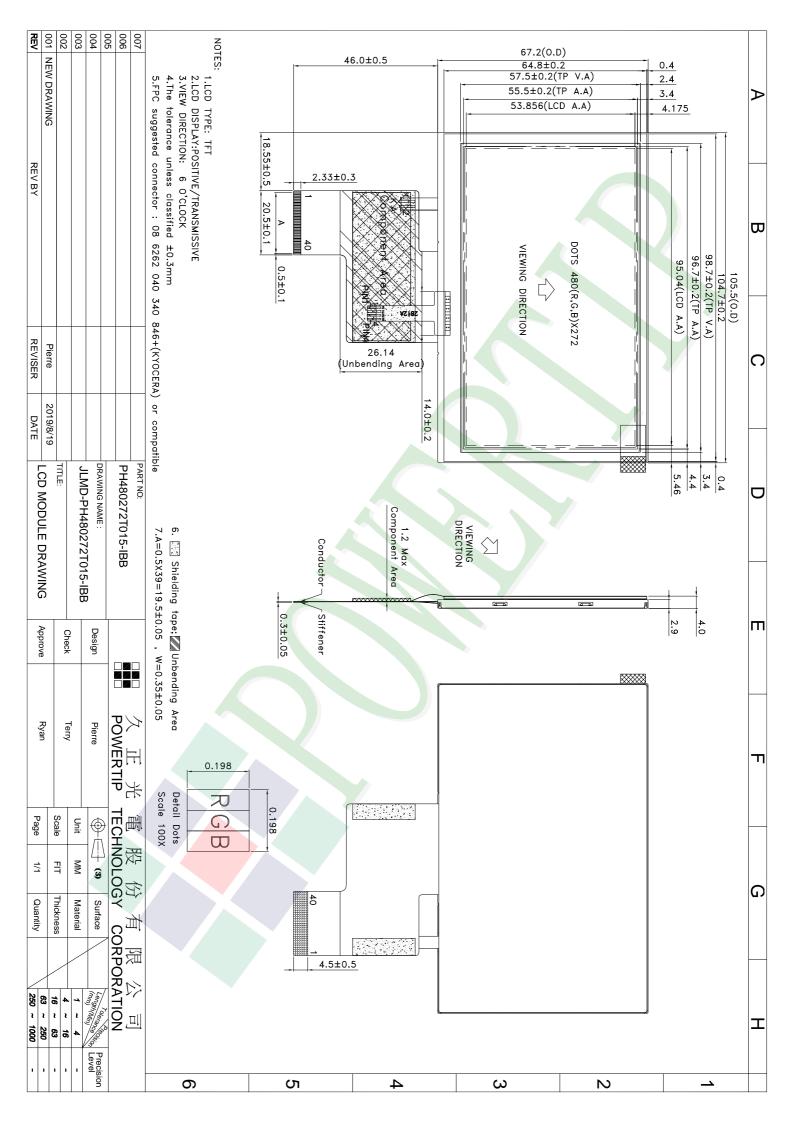
5.4 TERMS OF WARRANTY

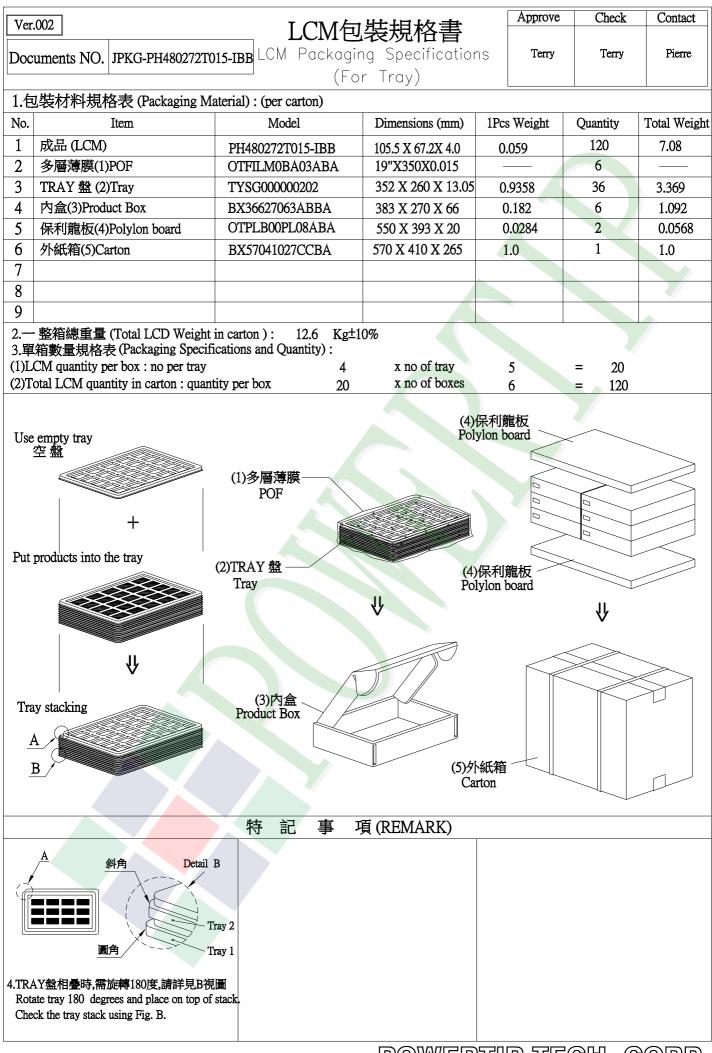
5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.





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